

**Patent 7,777,281**

**PATENT**

**IN UNITED STATES PATENT AND TRADEMARK OFFICE**

Patent No.: 7,777,281

Docket No: 2800.450US1

Issue Date: August 17, 2010

Patentee: Albert S. Weiner

Customer No.: 76287

Confirmation No.: 8458

Title            Non-volatile transistor memory array incorporating read-only elements with  
single mask set

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**REQUEST FOR CERTIFICATE OF CORRECTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
ATTN: CERTIFICATE OF CORRECTION BRANCH

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. A copy of the text of the Certificate in the suggested form is enclosed.

**Authorization to charge Deposit Account No. 19-0743 in the amount of \$100.00 to cover the Certificate of Correction Fee.**

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

The Examiner is authorized to charge any additional fees or credit overpayment to Deposit Account No.19-0743.

Respectfully Submitted,

SCHWEGMAN, LUNDBERG & WOESSNER, P.A  
P.O. Box 2938  
Minneapolis, MN 55402  
(612) 373-6900

Date : 29 September 2010

By: 

Robert E. Mates  
Reg. No: 35,271  
REM:rk

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 30 day of September, 2010.

Kevin Austad  
Name

  
Signature

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.  
(Also Form PTO-1050)

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,777,281

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DATED : August 17, 2010

INVENTOR(S) : Albert S. Weiner

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 4, line 2, delete "SEEPROM" and insert -- EEPROM --, therefor.

In column 4, line 6, delete "IT" and insert -- II --, therefor.

In column 4, line 22, in Claim 1, delete "region" and insert - - region; --, therefor.

MAILING ADDRESS OF SENDER:

SCHWEGMAN, LUNDBERG & WOESSNER, P.A.  
P.O. BOX 2938  
Minneapolis, MN 55402

PATENT NO. 7,777,281

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